

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/647,723	FOO ET AL.	
	Examiner	Art Unit	
	Vincent E. Kovalick	2629	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Applicant's Appeal Brief dated 7/25/07.
2. ☒ The allowed claim(s) is/are 1-5, 7-17 and 19-20 (re-numbered 1-18).
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

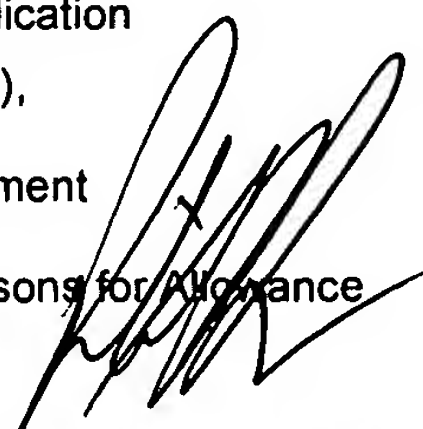
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
**RICHARD HJERPE**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2600**

### DETAILED ACTION

1. This Office Action is in response to Applicant's Appeal Brief Under 37 C.F.R. 41.37 (c) dated July 25, 2007 in response to USPTO Final Office Action dated March 20, 2007.

Applicant's Arguments regarding claims 1 and 10 have merit and are sufficient to place the application in a condition for allowance as set forth herein below.

#### *Allowable Subject Matter*

2. Claims 1-5, 7-17 and 19-20 are allowed.

3. The following is an examiner's statement of reasons for allowance:

Relative to claim 1, the major difference between the teachings of the prior art of record (Yasukawa et al. (US 2003/0210363); Santoro et al. (Pub. No. US 2003/0020671) and Martin (USP 6,094,704)) and that of the instant invention is that said prior art of record **does not teach** method of activating a display element of a display device having  $n \times m$  array of display elements, each display element coupled to a logic controlled switch, the method comprising: applying a row address input and a row electrode input to control logic of the logic controlled switch of the display element; applying a column address input and a column electrode input to the control logic of the logic controlled switch of the display element; activating the display element with the logic controlled switch when the row address and row electrode inputs and when the column address and column electrode inputs satisfy a condition.

Relative to claim 5, the major difference between the teachings of the said prior art of record and that of the instant invention is that said prior art of record **does not teach** a method in a display device comprising: an  $n \times m$  array of addressable display elements, the method comprising: activating at least some display elements characterizing a foreground image at a first rate; activating other display elements characterizing a background image at a second rate, the second rate less than the first rate; activating the display elements with a corresponding logic controlled display element switch when row address and row electrode inputs and when the column address and column electrode inputs satisfy a condition.

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Relative to claim 10, the major difference between the teachings of the said prior art of record and that of the instant invention is that said prior art of record **does not teach** a display device comprising: a plurality of display elements arranged in a matrix, each display element including a display pixel coupled to a switch, each display element including an addressable latch having an output coupled to a controlling input of the switch, the addressable latch having a row address input and a column address input.

Relative to claim 17, the major difference between the teachings of the said prior art of record and that of the instant invention is that said prior art of record **does not teach** a method in a display device comprising an  $n \times m$  array of addressable display elements, the method comprising: selectively activating display elements by individually addressing the display elements to be activated, activating the display elements includes, applying a row address input and a row electrode input to control logic of the corresponding display element, applying a column address input and a column electrode input to the control logic of the corresponding display element, and activating the display element with a logic controlled switch when the control logic inputs satisfy a condition; reducing power consumption by addressing at least some of the display elements at a first frequency and addressing other display elements at a second frequency, the second frequency less than the first frequency.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### ***Response to Applicant's Arguments***

5. Applicant's arguments relative to claims 1 and 10 are rendered moot in light of the allowance of this case.

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**Conclusion**

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U. S. Patent No.	6,476,914	Hoelzi et al.
U. S. Patent No	5,774, 104	Crossland et al.
Pub. No.	UIS 2006/0209009	Schlangen et al.


**To Respond**

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent E. Kovalick whose telephone number is 571-272-7669. The examiner can normally be reached on Monday-Thursday 7:30- 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

  
Vincent E. Kovalick  
August 17, 2007

  
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